

Slater & Matsil, L.L.P.

Suite 1000
17950 Preston Road
Dallas, Texas 75252-5793
Phone: 972-732-1001 Facsimile: 972-732-9218

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Kuzmenka, et al. Docket No.: INF-129
Serial No: 10/759,103 Art Unit: 2818
Date Filed: January 20, 2004
Title: Buffer Amplifier Architecture for Semiconductor Memory Circuits

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- Information Disclosure Statement (1 page)
- IDS Form PTO/SB/08a (1 page) citing (2) references

Respectfully submitted,

Natalie Swider

Natalie Swider
Legal Assistant

Confirmation Respectfully Requested

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OCT 05 2004

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Applicant: Kuzmenka, et al. Attorney Docket: INF-129
Filed: January 20, 2004 Examiner: TBD
Serial No.: 10/759,103 Art Unit: 2818
For: Buffer Amplifier Architecture for Semiconductor Memory Circuits

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Dear Sir:

Applicants wish to bring to the attention of the Patent and Trademark Office the information noted on the enclosed form PTO/SB/08a that may be considered material to the examination of the above-identified application.

No fee is due at this time, as this Information Disclosure Statement is being filed pursuant to 37 C.F.R. § 1.97(b)(3), before the mailing of a first Office action on the merits.

Respectfully submitted,



Ira S. Matsil
Attorney for Applicants
Reg. No. 35,272

10/5/2004

Date

Slater & Matsil, L.L.P.
17950 Preston Rd., Suite 1000
Dallas, TX 75252
(972) 732-1001 (phone)
(972) 732-9218 (fax)

PTO/SB/08a (08-03)

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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Use as many sheets as necessary)</i>				Complete If Known	
				Application Number	10/759,103
				Filing Date	1/20/2004
				First Named Inventor	Kuzmenka, et al.
				Art Unit	2818
				Examiner Name	TBD
				Attorney Docket Number	INF-129
Sheet	1	of	1		

[illegible][illegible]

Examiner Signature		Date Considered	
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